

15507108

ARM

Design and Implementation of ARM Intelligent Vehicle Control System

1501

2019 5

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ARM

PID

ARM Cortex-M3

ARM

LM3S615

1024

IRM8601S

ARM

ARM

PID

Abstract

With the continuous development of the automotive industry, with the support of science and technology, automotive intelligence has become a trend. Traditional automobile driving is mainly manual operation, and drivers are sometimes disturbed by other factors, so there are certain safety risks. The design and use of intelligent vehicles can not only help people save resources, but also solve the work that people cannot complete. This paper combines ARM embedded chip, designs and implements intelligent vehicle control system, aiming at continuously promoting the development of automobile intelligence.

Based on the incremental PID control algorithm and graph theory algorithm, the ARM intelligent car control system is designed and implemented. With ARM Cortex-M3 microcontroller LM3S615 as the core control chip, the speed of the car is output by an integrated 1024-line encoder, and the speed and direction parameters are fed back to the DC motor and gyroscope. The wall information is detected and fed back by the integrated infrared sensor IRM8601S, and the contour map is used to realize the automatic driving, obstacle avoidance and road search of the ARM intelligent car. The best choice is the best. Optimal path and other functions.

Key Words: ARM Embedded Incremental PID Control Intelligent Vehicle
Graph Theory Closed-loop Control

1

ARM

ARM

[1]

1 1

ARM

ARM

1 2

50

1954

1986

1995

1996

-

Navlab

21

European Land Robot Trial

DARPA

1 1992

2 2003

130 / ~170 /

3 2006

Cyber Car

4 2014

10

863

[2]

5 2018

1 3

1 4

ARM

2

2 1

ARM

1 ARM
2 ARM
3 ARM

2 2

ARM

1
2
3
4
5
6

2 3

1

ARM

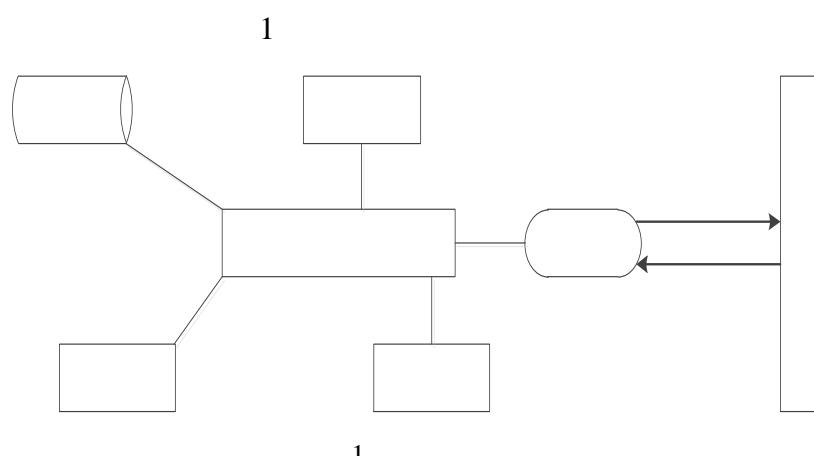
2
 ν θ

5m/s
7.4v/350mA·h
96mm 84mm
300g

3

ARM

3 1



1

3 2

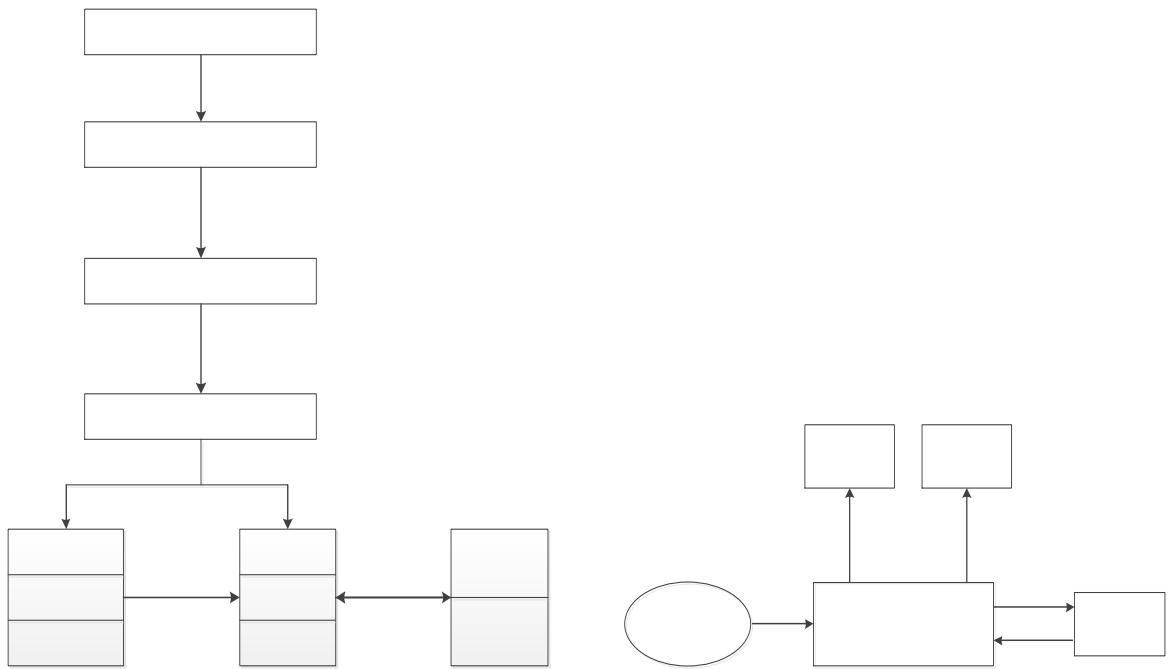
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5



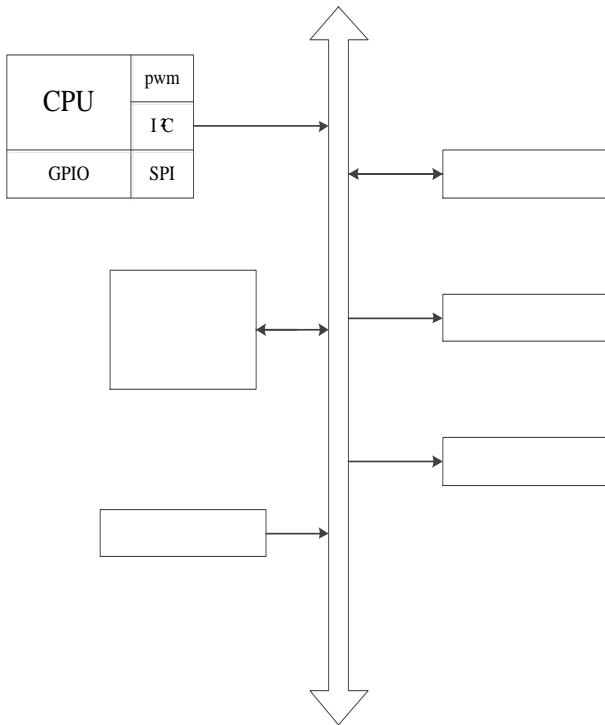
2

3

3 3

CPU

4



4

4

4 1

4 1 1

LM3S615

ARM®

CortexTM-M3

8 16

32

[3-6]

4 1 2

1717SR

7.4V

1.96W

14000rmps^[7-8]

1

PWM

5

PWM

V1

Ui

PWM

V1

Us

t1

Ui

V1

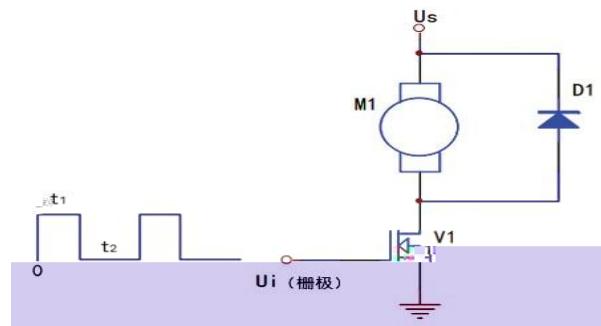
t2

Uo

1

$$U_o = (t1 * U_s) / (t1 + t2) = U_s * (t1 / T) = \alpha * U_s \quad (1)$$

α

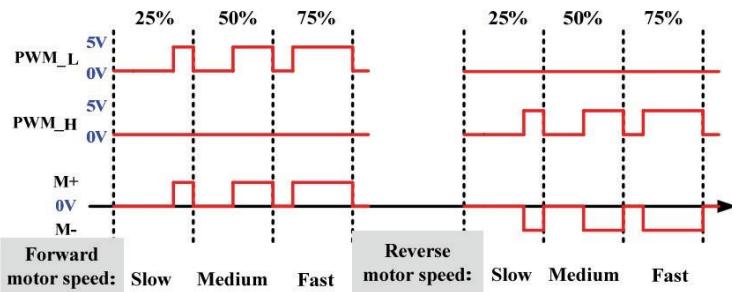


5 PWM

PWM

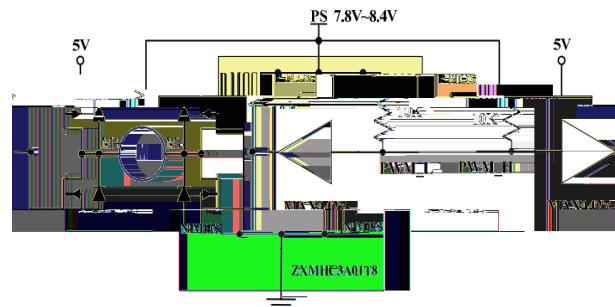
6

PWM [9-10]



6 PWM

2



7

7

PWM

PWM_H L PWM_L H

PWM_H H PWM_L L

PWM_H L PWM_L L

PWM_H H PWM_L H

PWM

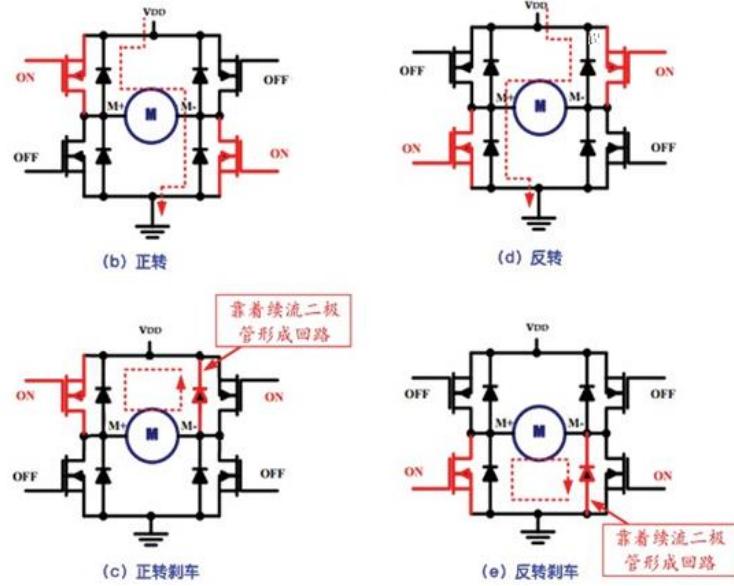
2

8

2

PWM_H	PWM_L
L	L
L	H
H	L
H	H

8



8

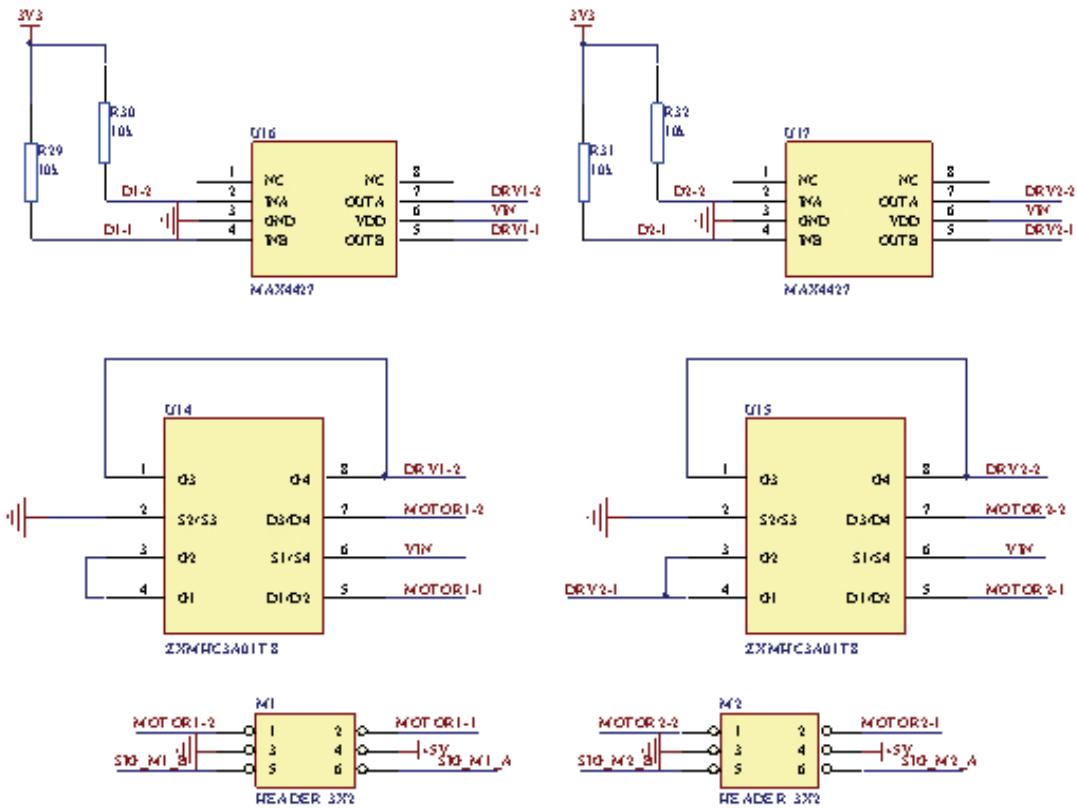
3

9

MAX4427

HEADER

ZXMHC3A01T8 H



9

9

4 1 3

ARM

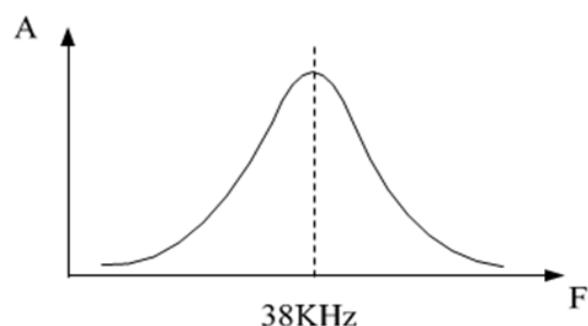
6

38KHz

[11-13]

10

38KHz



10

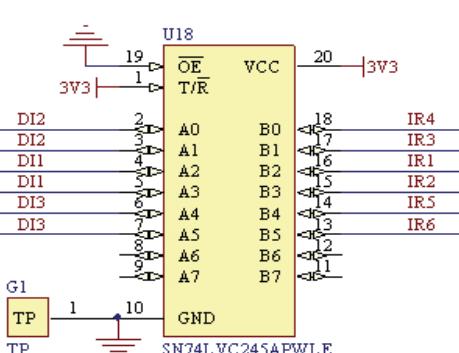
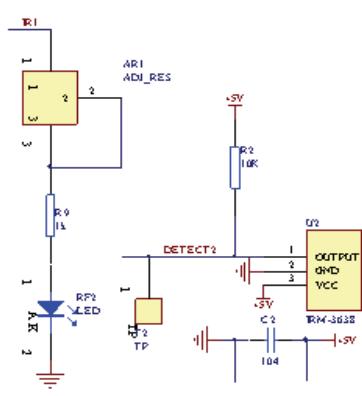
ARM

11

IR1

SN74

4.2

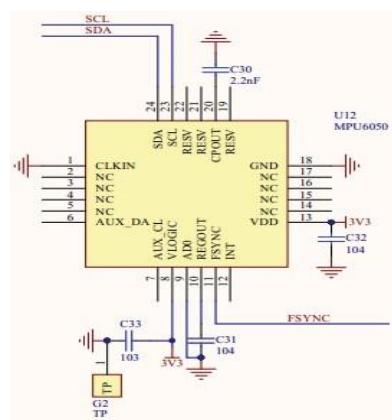


11

10

4 1 4

16 ADC^[14-16]



MPU6050

MPU6050

4 1 5

4 2

4 2 1

1 PID

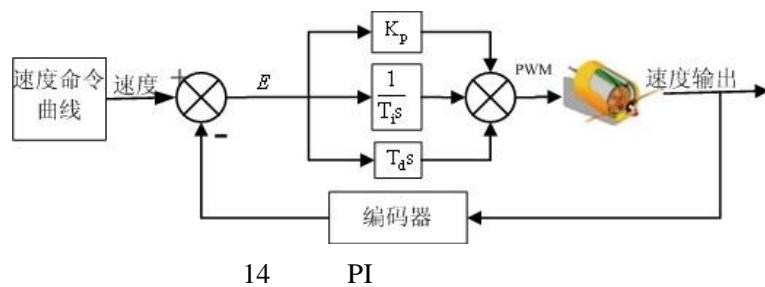
PWM

PWM

PI

PI

14



14

e PI

PWM

DC

e 0

PID

ΔU_k

ΔU_k

PID

2

[22]

2

k-1

3

$$u_k = k_p [e_k + \frac{T}{Ti} \sum_{j=0}^k e_j + Td \frac{e_k - e_{k-1}}{T}] \quad (2)$$

$$u_{k-1} = k_p [e_{k-1} + \frac{T}{Ti} \sum_{j=0}^{k-1} e_j + Td \frac{e_{k-1} - e_{k-2}}{T}] \quad (3)$$

2

3

PID

4

$$\begin{aligned}
 \Delta u_k &= u_k - u_{k-1} = k_p(e_k - e_{k-1} + \frac{T}{Ti} e_k + Td \frac{e_k - 2e_{k-1} + e_{k-2}}{T}) \\
 &= k_p(1 + \frac{T}{Ti} + \frac{Td}{T}) e_k - k_p(1 + \frac{2Td}{T}) e_{k-1} + k_p \frac{Td}{T} e_{k-2} \\
 &= Ae_k + Be_{k-1} + Ce_{k-2} \tag{4}
 \end{aligned}$$

$$A = k_p(1 + \frac{T}{Ti} + \frac{Td}{T})$$

$$B = k_p(1 + \frac{2Td}{T})$$

$$C = k_p \frac{Td}{T}$$

4

T A B C

4

$$k_p \quad k_i$$

$$k_i \quad 0 \quad k_p$$

15-a

$$k_i$$

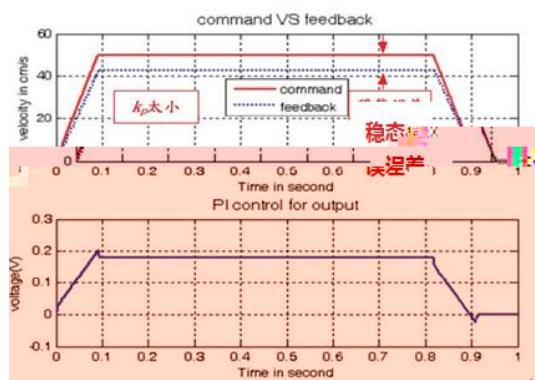
$$15-b$$

$$k_p$$

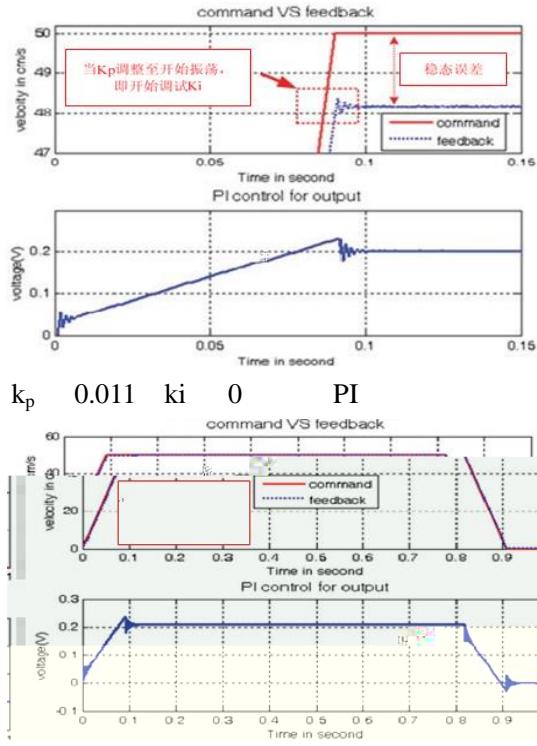
$$15-c$$

Feedback

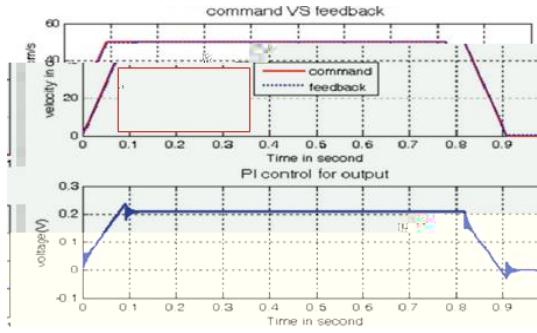
command



$$a. \quad k_p \quad 0.025 \quad k_i \quad 0 \quad PI$$



b. $k_p \quad 0.011 \quad k_i \quad 0 \quad \text{PI}$



c. $k_p \quad 0.011 \quad k_i \quad 3 \quad \text{PI}$

15

2

16

PI

PI

xSpeed

$$2 \quad \text{rightSpeed} + \text{leftSpeed} / 2$$

$$\text{xError} \quad \text{xError} = \text{xSpeed} - \text{R} + \text{L} / 2$$

xError

PI

U1

PI

U1

PWM

xError

0

xSpeed = R + L / 2

PI

PI

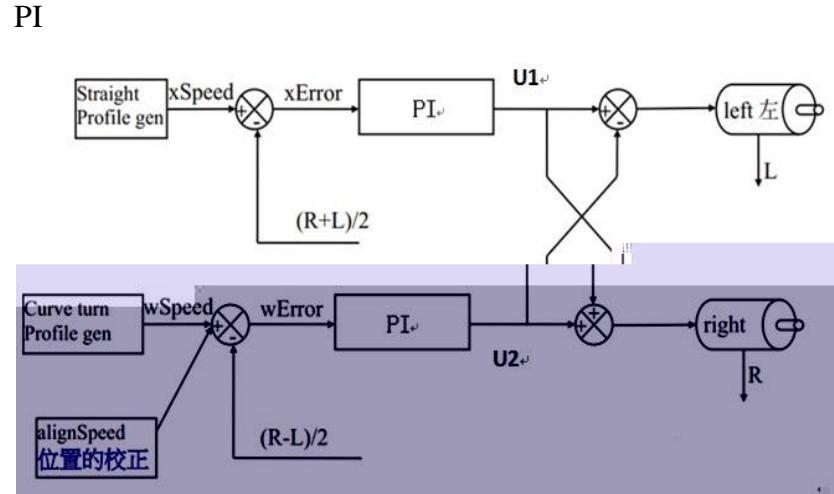
wSpeed

$$2 \quad \text{rightSpeed} - \text{leftSpeed} / 2$$

$$\text{wError} \quad \text{wError} = \text{wSpeed} - \text{R} - \text{L} / 2$$

wError

PI U2 PI U2
 PWM wError 0 wSpeed= $R-L / 2$



16

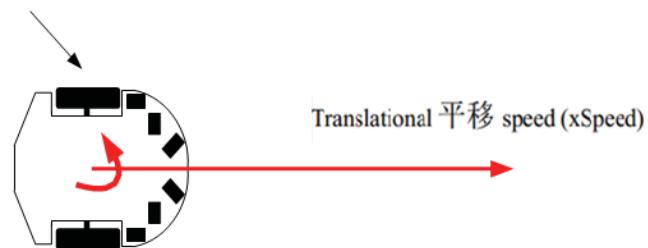
$$\text{leftSpeed} = \text{xSpeed} - \text{wSpeed}$$

$$\text{rightSpeed} = \text{xSpeed} + \text{wSpeed}$$

$$\text{xSpeed} = \text{rightSpeed} + \text{leftSpeed} / 2$$

$$\text{wSpeed} = \text{rightSpeed} - \text{leftSpeed} / 2$$

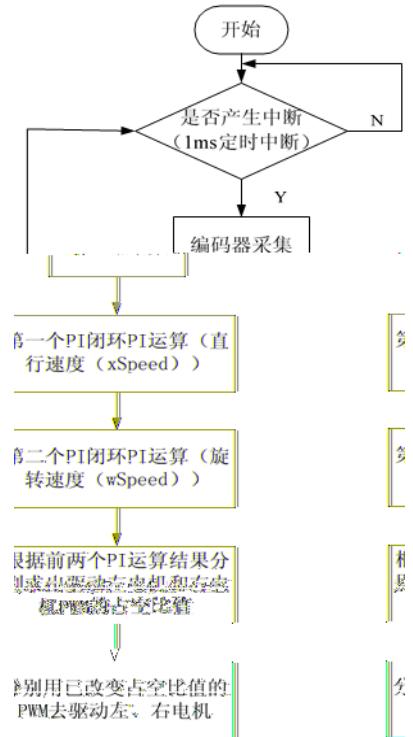
Rotational 旋转 speed(wSpeed)



17

U1 U2 U1 U2
 PI PI 17 PI

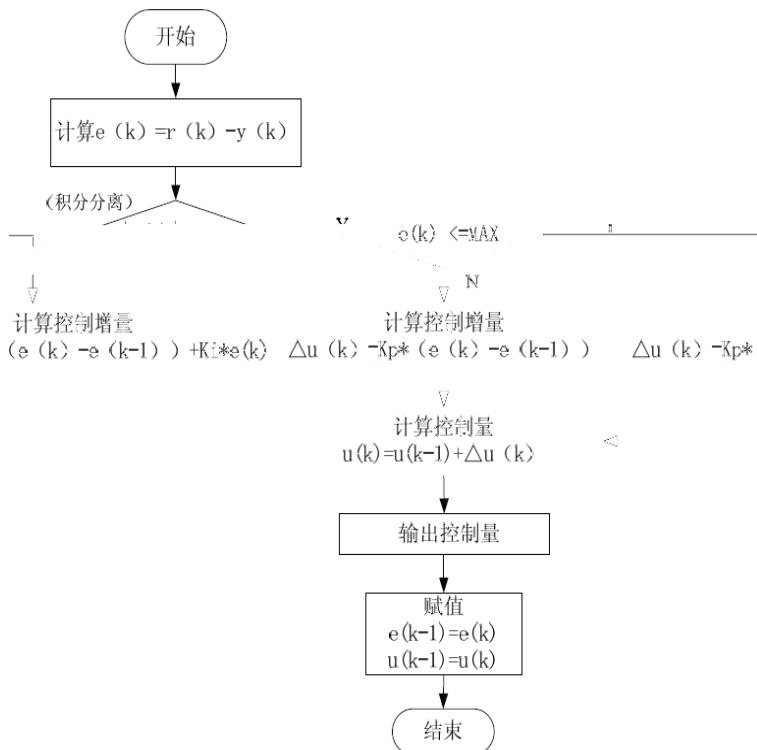
PWM e t1 t1 T PWM



18

PID

19



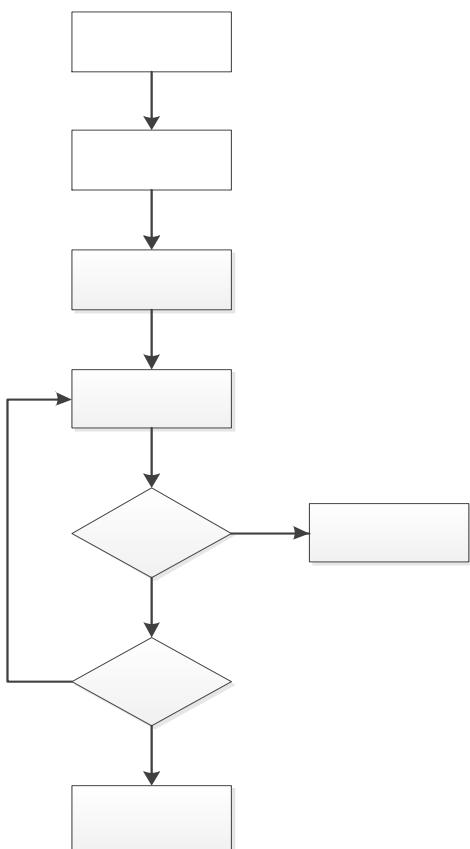
19

PID

4 2 2

1

20



20

2

```
void __sensorInit(void)
{
    TimerDisable(TIMER1_BASE, TIMER_B);
    TimerControlLevel(TIMER1_BASE, TIMER_B, true);
    TimerDisable(TIMER1_BASE, TIMER_B
        | __IRSEND3_LR);
    PWMGenConfigure(PWM_BASE, PWM_GEN_1, PWM_GEN_MODE_UP_DOWN
        | __IRSEND3_LR);
    PWMOinPutState(PWM_BASE, PWM_OUT_7_BIT
```

```

|PWM_OUT_3_BIT,false);

PWMGENEYable(PWM_BASE, PWM_GEN_7);

TimerControlLevel(TIMER1_BASE,TIMER_B,true);

TimerDisable(TIMER1_BASE,TIMER_B,
            __IRSEND3_LR);

TimerDisable(TIMER1_BASE,TIMER_B);

TimerDisable(TIMER1_BASE,TIMER_B);

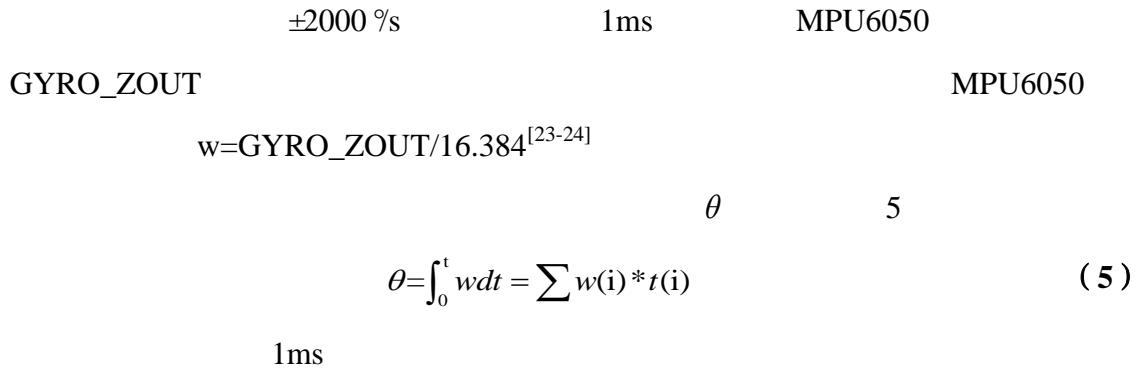
TimerControlLevel(TIMER1_BASE,TIMER_B,true);

}

```

4 2 3

1



2

```

void GYRO_Z_Angle(void)
{
    int16 w=0;
    zlg7289Download(0,0,0,GW%10);
    w=w/16.4;
    GWH=GWH+w;
}

main (void)

```

```

{
    int16 Val;

    TimerControlLevel (TIMER1_BASE,TIMER_B,true _MAIN
                        |__IRSEND3_LR);

    TimerControlLevel(TIMER1_BASE,TIMER_B,true);
    TimerDisable(TIMER1_BASE,TIMER_B);

    zlg7289Download(0,0,0,GW%10);

    mazeInit();

    MPU6050_init(1);

    zlg7289Download(0,0,0,GW%10)

    {

        GYRO_Z_Angle();

        zlg7289Download(0,3,0,GW%10);

        zlg7289Download(0,0,0,GW%10);

        zlg7289Download(0,0,0,GW%10);

        zlg7289Download(0,3,0,GW%10);

        mazeInit();

    }

}

```

4 2 4

1

0

1

2

3

2

3

3

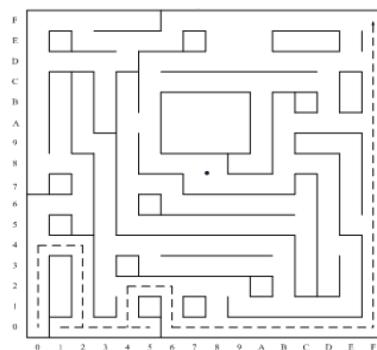
bit0	0	1	0
bit1	1	1	0
bit2	2	1	0
bit3	3	1	0

3

1

21

0 0

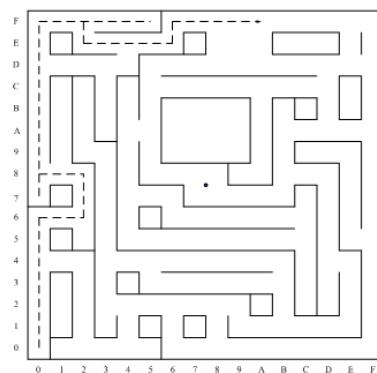


21

2

22

0 0



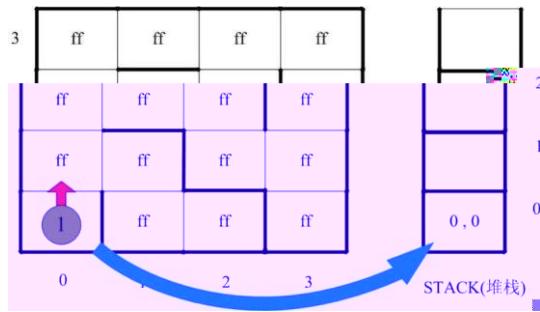
22

20

3

23

1 2 3



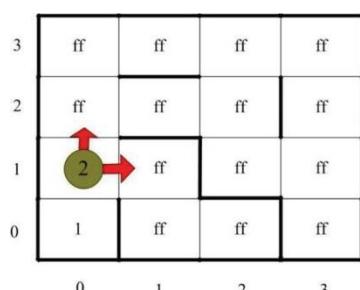
25

25

0 0

0 1 Step=Step+1

26



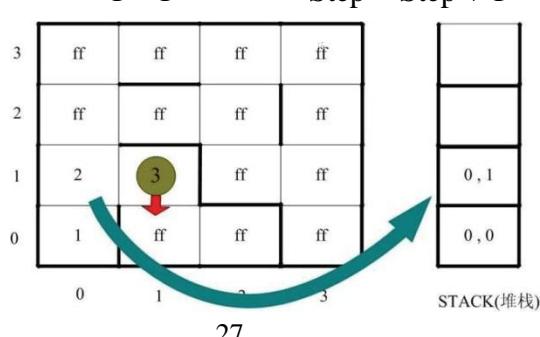
26

26

0 1

1 1 Step = Step + 1

27



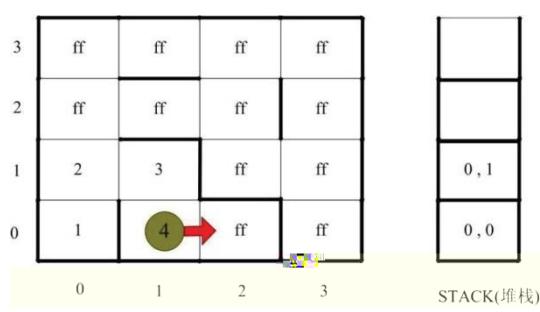
27

27

1 0

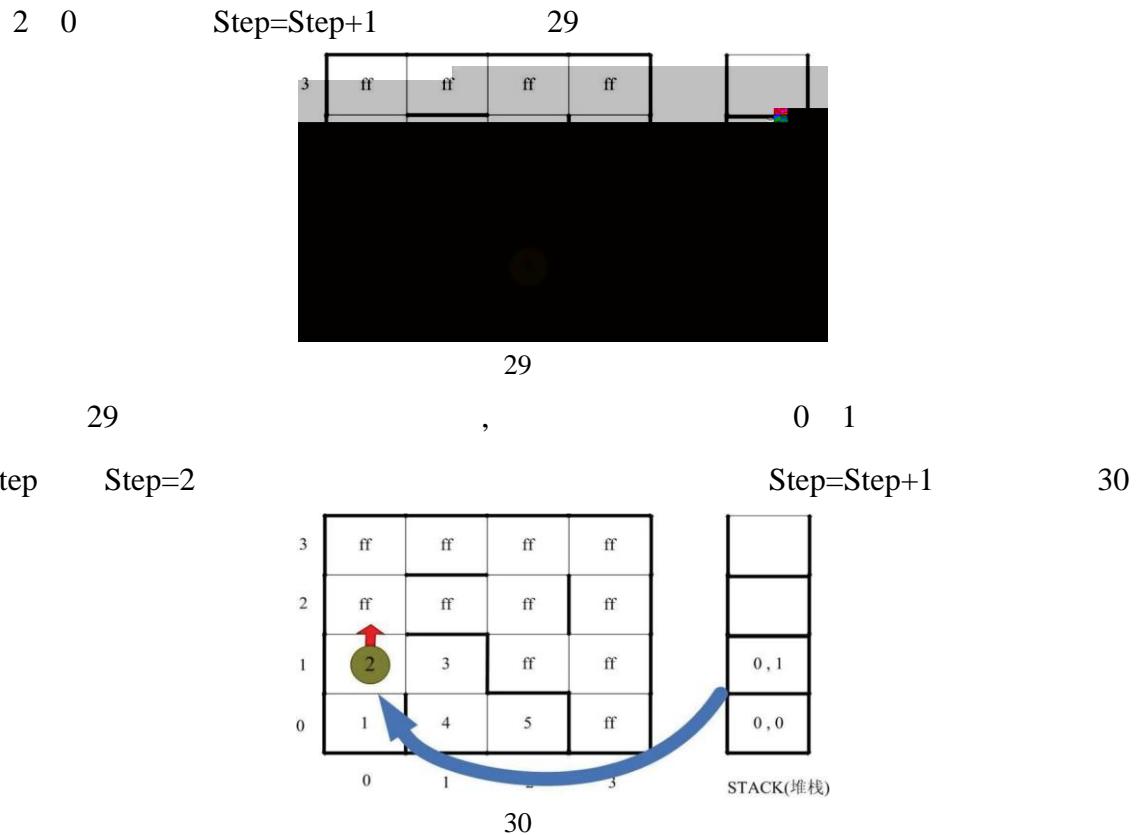
Step=Step+1

28

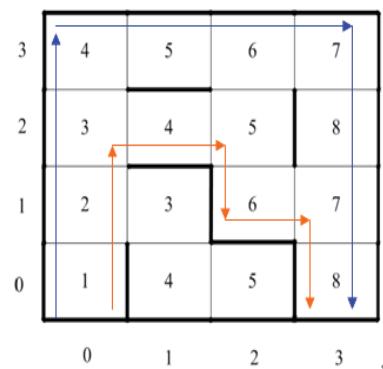


28

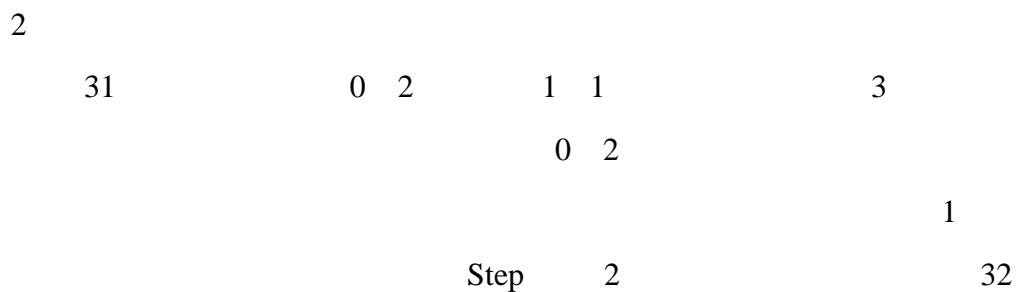
28

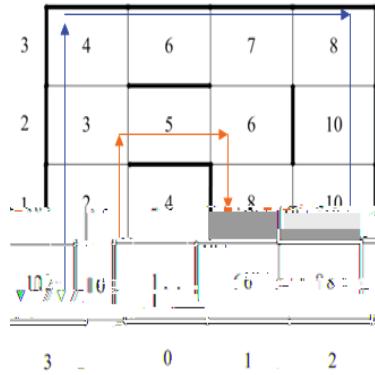


31



31





32

4 2 5

1

1

0

PI

R+L /2

PI

PI

xError

0 xSpeed= R+L /2

PI

wError

0

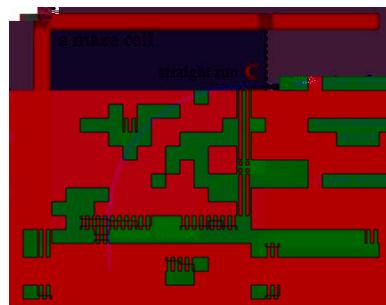
wSpeed= R-L /2=0

R=L

xSpeed

2

33



33

90 °

PI

		PI
xSpeed	R+L /2	PI
		xError
R-L /2		0
xSpeed= R+L /2=		PI
		wError
wSpeed= R-L /2=		0
		R>L>0

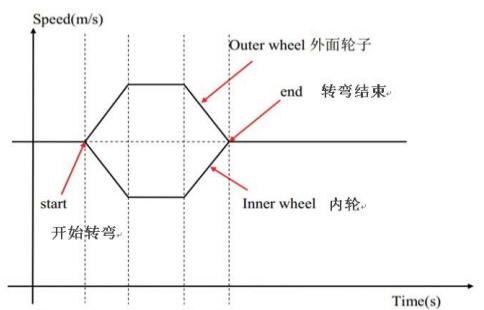

```

void mouseTurnleft(void)
{
    while((GmRight.uiPulseCtr+200)<=GmRight.uiPulse);
    while((GmLeft.uiPulseCtr+200 )<=GmLeft.uiPulse);
    GmLeft.uiPulse =3000;
    GmLeft.uiPulseCtr=0;
    GmRight.uiPulse =3000;
    GmRight.uiPulseCtr=0;
    mazeInit();
    break;
    GucMouseState=TURNLEFT;
    GucMouseDir=(GucMouseDir+3)%4;
    while(1)
    {
        if(GPIOPinRead(GPIO_PORTD_BASE, __DIR1) == 0)
        {
            break;
        }
    }
    while((GmRight.uiPulseCtr+200)<=GmRight.uiPulse);
    while((GmLeft.uiPulseCtr+200)<=GmLeft.uiPulse);
}

```

xSpeed	$R+L/2$	PI	
$R-L/2$		PI	xError 0
$xSpeed = R+L/2 =$		PI	wError 0
$wSpeed = R-L/2 =$			$0 < R < L$

34



34

2

ARM

1

2

0 0

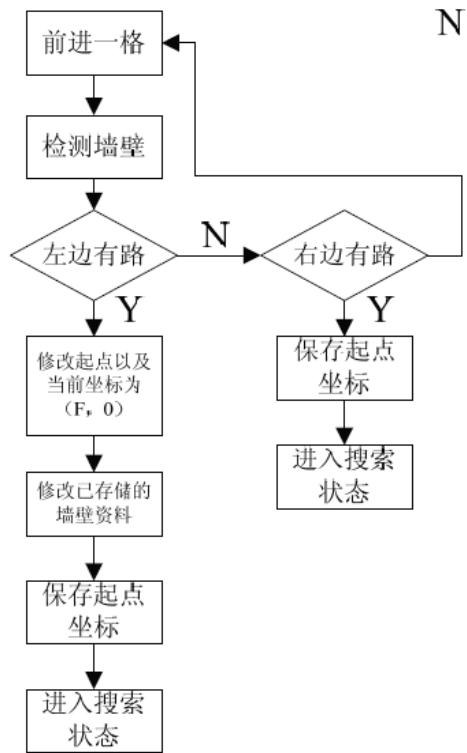
F 0

35

3

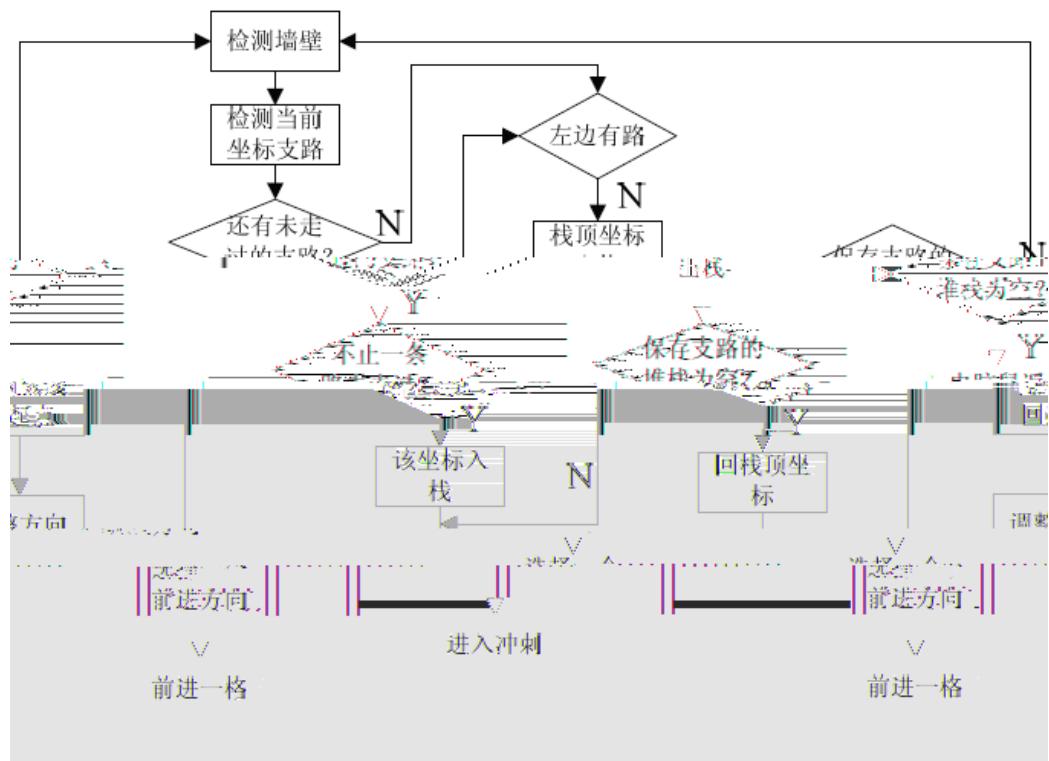
36

4



N

35



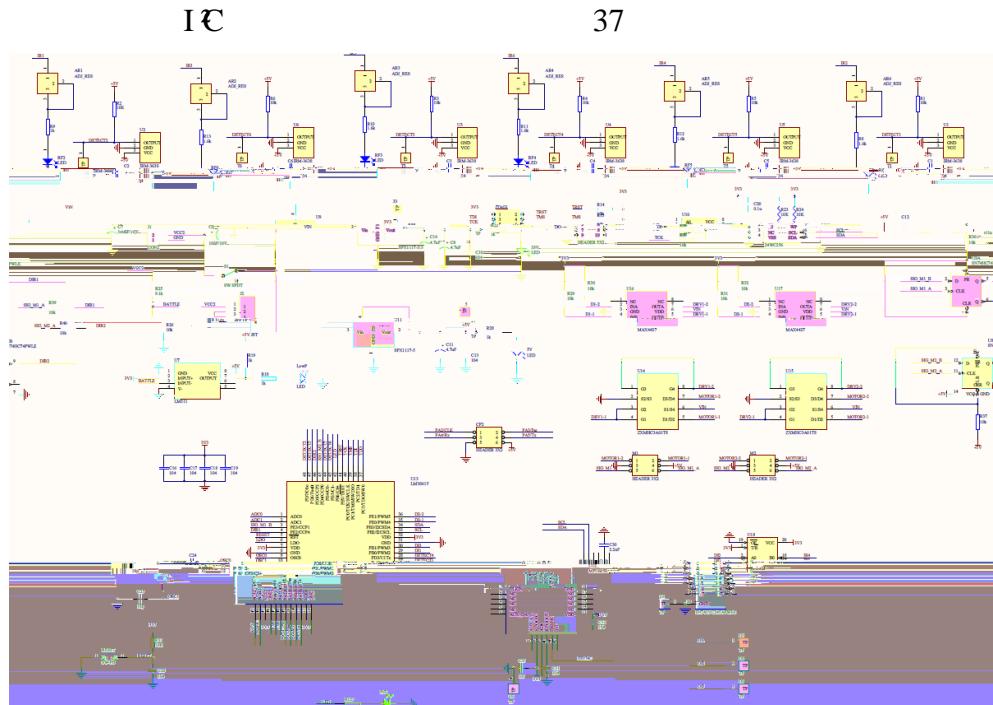
36

27

5

5 1

ARM LM3S615 PWM GPIO SPI /



37

PWM D1_1 D1_2 D2_1 D2_2 M4427 INB

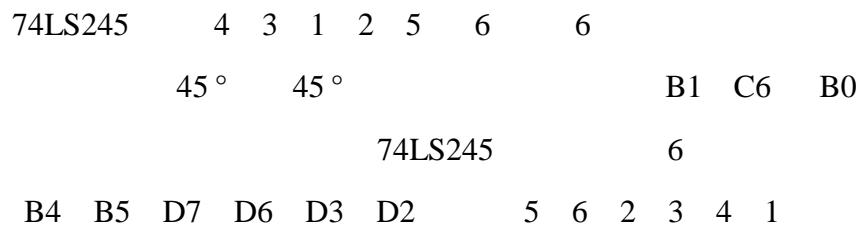
INA D1_1 D1_2 M4427

INB INA D2_1 D2_2

E D

SIG_M1_A SIG_M2_A SIG_M1_B SIG_M2_B D CLK

D



SDA SCL FSYNC GPIOB3 GPIOB2 GPIOC4

	MPU6050	SCL	SDA	FSYNC
A1	A0		AT24C08	
	GPIOB3	GPIOB2		RESET C7 C5

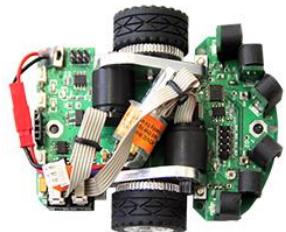
38 39 40 41



38 ARM



39 ARM



40 ARM

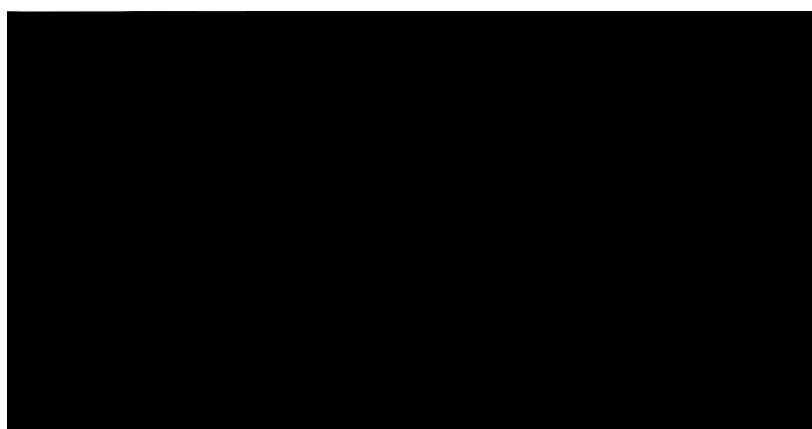


41 ARM

IAR EWARM

Build

42



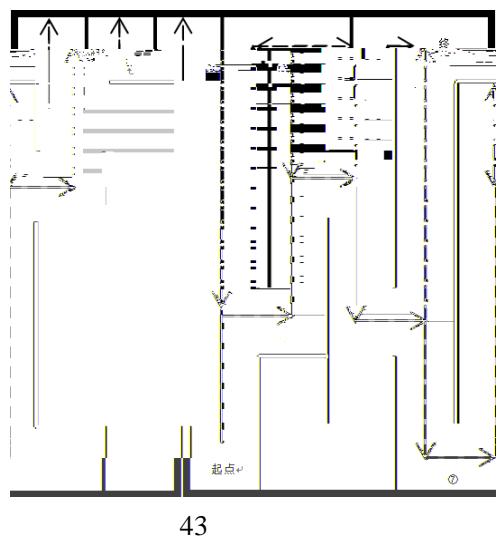
42 Build

5 2

ARM

43

4



43

4

1
2
3
4
5
6
7

5 3

1

ARM

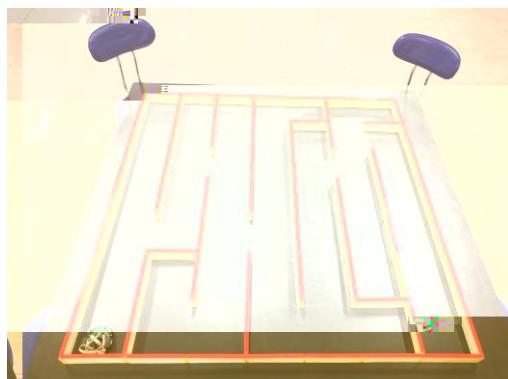
ARM	7*7				
3*3		1	3*4		2
4*3	3	4*4	4	1	
2		3		4	
ARM	Step	ARM			
0 0	6 6				

ARM

0 0

44

45



44

45

45

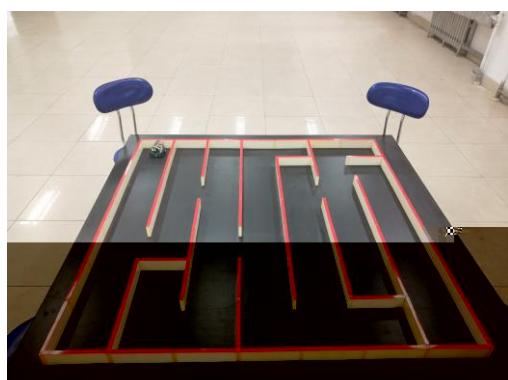
1

0 6

46

0 3

47



46

47

1 6

1 4

3

48

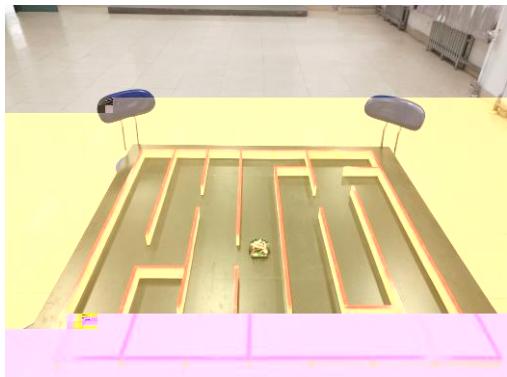


48

2 2

2

3 0



49

49

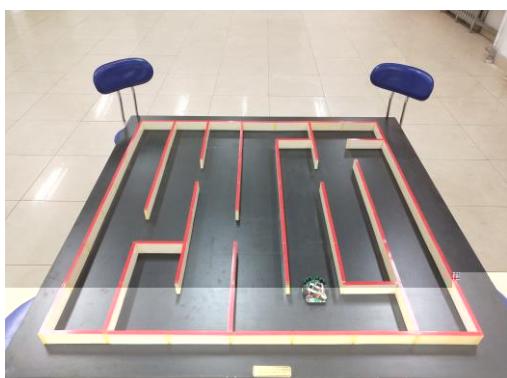
3 6

5 0



50

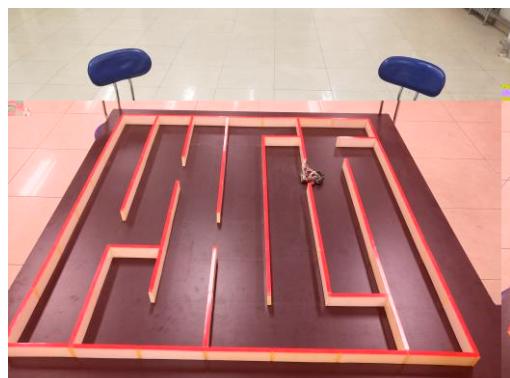
4 5



51

4

52



52

4

6 6

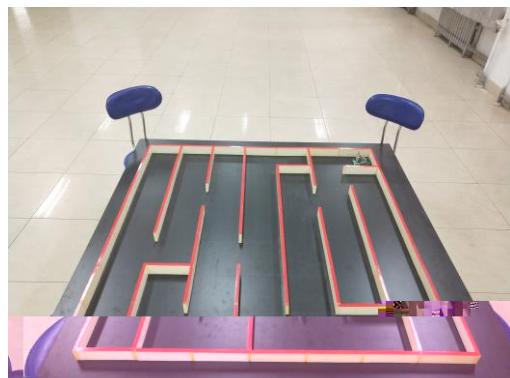


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ARM

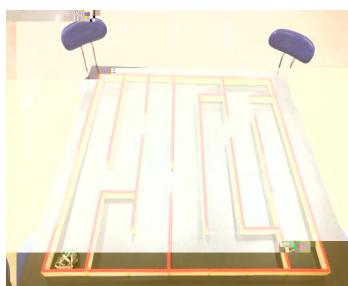
ARM

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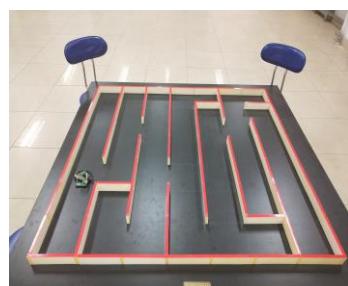
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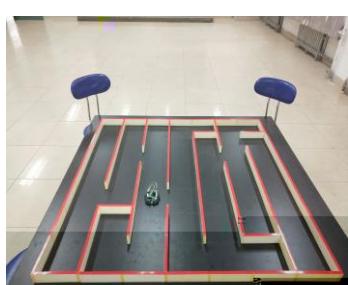


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1 Cortex-M3 LM3S615

2 1717SR 1024

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4 MPU-6050

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